**DAILY ASSESSMENT FORMAT**

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| **Date:** | **28 MAY 2020** | **Name:** | **Nithyashree S** |
| **Course:** | **Logic design** | **USN:** | **4AL18EC036** |
| **Topic:** | **1.Analysis of clocked sequential circuits.**  **2.Digital clock design.** | **Semester & Section:** | **4th sem & A section** |
| **Github Repository:** | **Nithyashree-S** |  |  |

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| **FORENOON SESSION DETAILS** |
| **Image of session** |
| **Report – Report can be typed or hand written for up to two pages.**       |  |  |  |  | | --- | --- | --- | --- | | **DATE:** | **28 MAY 2020** | **NAME:** | **Nithyashree S** | | **COURSE:** | **PYTHON** | **USN:** | **4AL18EC036** | | **TOPIC:** | **Object oriented programming.** | **SEMESTER & SECTION:** | **4th sem & A section** | |

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| **AFTERNOON SESSION DETAILS** | | | |
| **Image of session** | | | |
| **Report – Report can be typed or hand written for up to two pages** | | | |